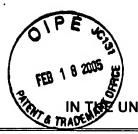
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In re Application of:

Takashi KOBAYASHI

Serial No.: 10/020,573

Filed: October 31, 2001

Title: VERTICAL INSULATED GATE SEMICONDUCTOR DEVICE

Group Art Unit: 2826

Examiner: Nathan Flynn

Attorney Docket No.: FUJI:200

COMMISSIONER FOR PATENTS

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RESPONSE TO NOTICE REGARDING DRAWINGS

Sirs:

Please substitute the attached one sheet of formal drawing 24/34 depicting Figure 28 for the drawings filed on March 11, 2002.

Respectfully submitted,

ROSSI, KIMMS & McDOWELL LLF

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